



PATENT
Docket No.: ACT-280COA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2116

Examiner: Eric Chang

Serial No.: 10/722,636

Filed: November 25, 2003

In re Application of: Plants et al.

For: DELAY LOCKED LOOP FOR AN FPGA ARCHITECTURE

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 10/15/04.

Signed


Stephanie Davis

TRANSMITTAL LETTER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find a Response to the Office Action dated July 15, 2004 in connection with the above-identified application.

In the event the patent office charges a fee for filing the above-noted documents, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, the Assistant Commissioner is hereby authorized to charge or credit the difference to our Deposit Account No. 50-0612. An additional copy of this page is enclosed.

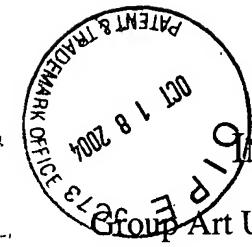
Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Dated: October 15, 2004



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AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated July 15, 2004, kindly amend the above-identified application as follows.